

README and Design for 2DArray Memory

This design for a 2DArray Memory (similar to the grid memory using 2 BRAMs example) implements a 64x32 grid of 8x8 pixel cells. Each cell holds a 16-bit value (which could distinguish 65,536 objects, but this design only sends 2 bits/cell for 4 objects).

To use this either MicroBlaze or a FSM would write the value of an object to the grid cell location (row/column) in the BRAM. When Scopeface reads the BRAM, it then draws the proper 8x8 pixel object on the screen at the cell location. The colors and shape of each type of object are defined in scopeface.

For a 64x32 grid we use 6 bits to specify the cell column and 5 bits to specify the cell row, then $2^6 \times 2^5 = 64 \times 32 = 2048$ cells. [see design on the following pages]

This design is modified from Lab2 and uses video/scopeface to read from the 2Darray memory to display on the monitor through the HDMI port.

This Scopeface's ch1 and ch2 two accepts the 2 bits per pixel as:

- 00 scopeface background art
- 01 Object#1
- 10 Object#2
- 11 Object#3

If you want Scopeface to always draw an unchanging static image, it can use 00 to ignore the objects and draw the scopeface.

The bit file is included. If you'd like to build the project from the included files, you will need to recreate clock_wiz_0 similar to lab1.

The hardware FSM is set up to run a test image:

- Let exSel = '0' in graphics.vhd to run the test.

- The FSM has a delay (using a counter) between writes, so this can write much faster if you decrease the delay count.

To Use with MicroBlaze:

- Set up slv_regs to write to exSel, exRow, exCol, exData, and exWen

- Initialize exSel <= '1'

- For each cell to write to, let

- exRow <= the cell row to write to

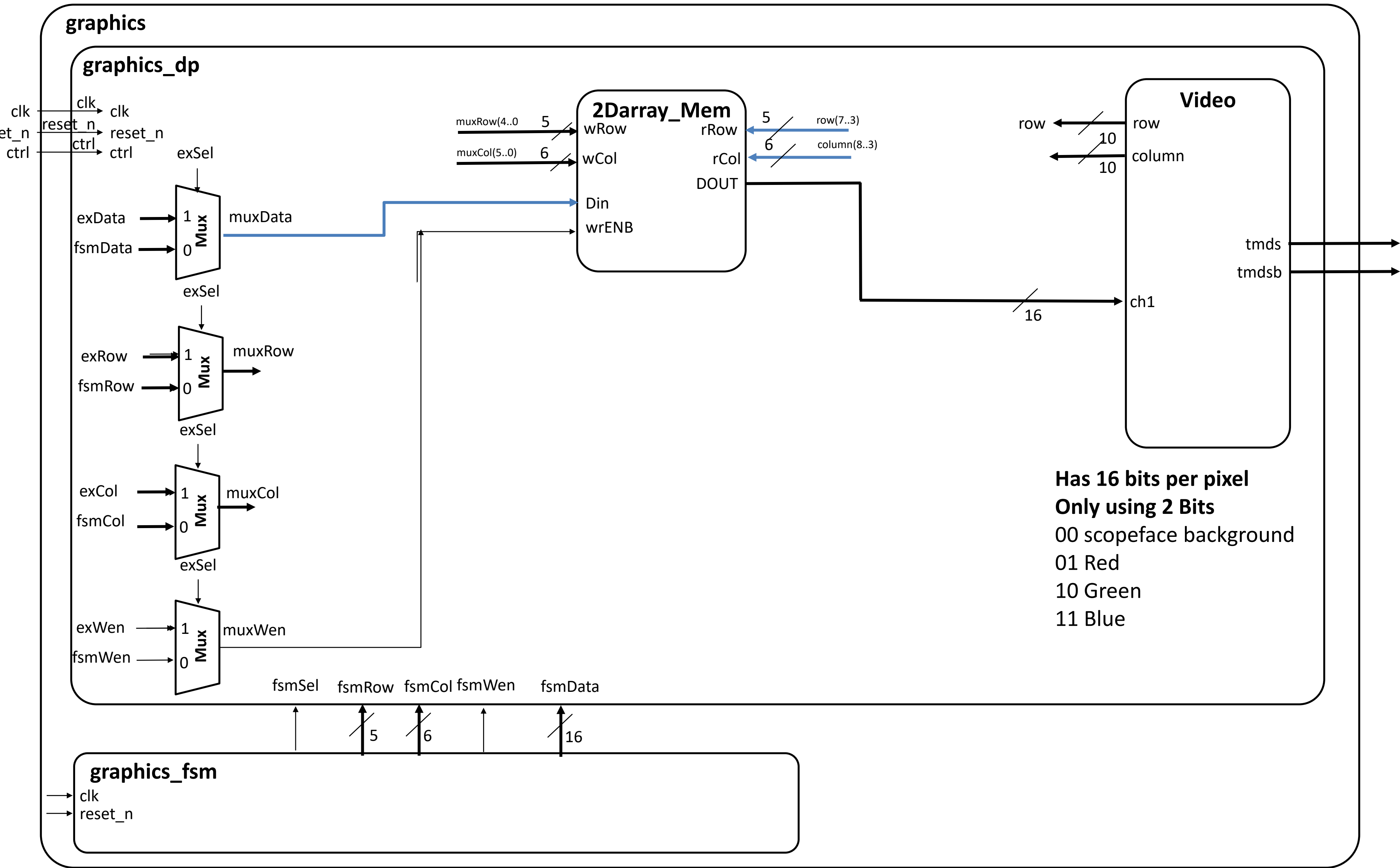
- exCol <= the cell column to write to

- exData <= the object to draw in the cell

- exWen <= '1', to write

- exWen <= '0' to stop writing

- repeat for the next cell





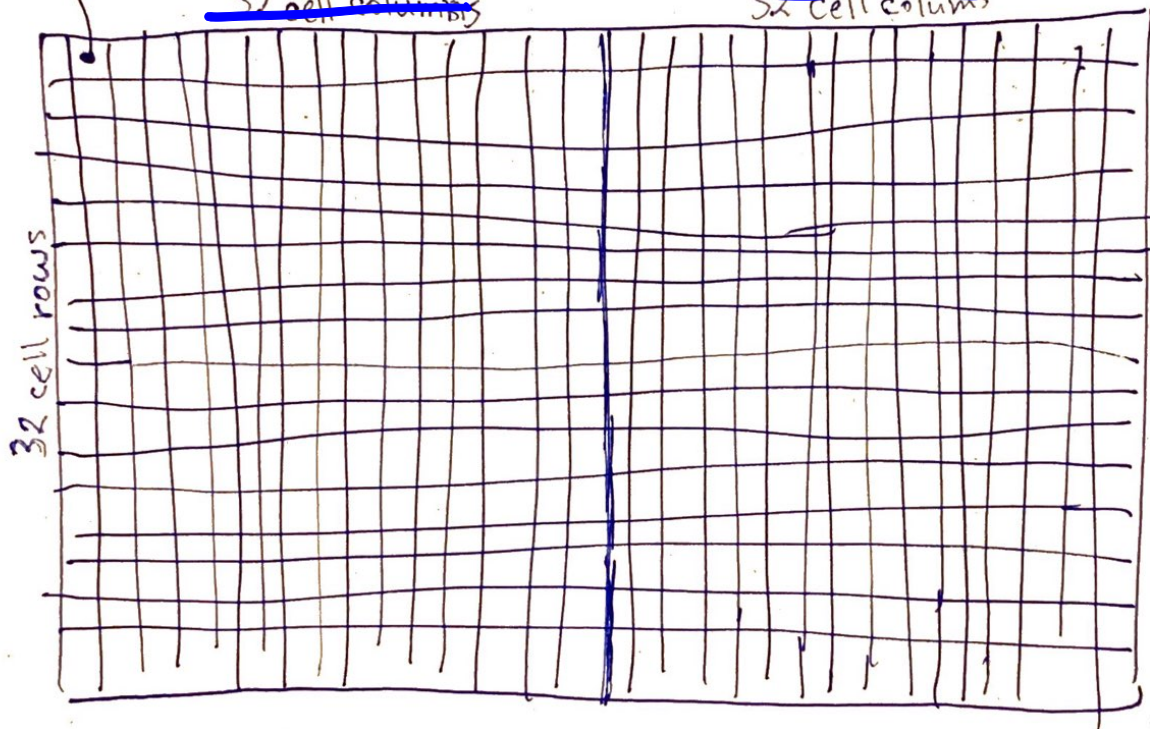
~~32 cell columns~~

64 cell columns

~~32 cell columns~~

32 cell columns

32 cell columns



64 cell columns \rightarrow 6-bits

32 cell rows \rightarrow 5-bits

	Scopeface		Pixel
	Extra	Cell	
row	2-bits	5-bits	3 bits
	Extra	Cell	Pixel
col	1-bit	6-bits	3 bits